

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTORS: Hooli et al.

SERIAL NO.: 10/783,049

ART UNIT:

FILED: 2/19/04

EXAMINER:

TITLE: Chip-Level or Symbol-Level Equalizer Structure for Multiple Transmit

And Receiver Antenna Configuration"

ATTORNEY DOCKET NO.: 872.0178.U1(US)

Commissioner For Trademarks

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Docket No.: 872.0178.U1(US)

Serial No.: 10/783,049

(FORM PTO-1449)

Applicant(s): Hooli, et al.

(Substitute) Filing Date: 2/19/04 Group:							
		U.S. PATENT I	OCUMENTS				
Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Pa	tentee or Applicant	Class	Sub-class	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name	Of Patentee of Applicant		Translation? Yes/No/n/a	
OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)							
	"Chip-Level Channel Equalization in WCDMA Downlink", Kari Hooli et al., EURASIP Journal on Applied Signal Processing 2002-8, pp. 757-770						
	"A Generalized RAKE Receiver for Interference Suppression", Gregory E. Bottomley, et al., IEEE 2000, pp. 1536-1545						
	"V-BLAST: An Architecture for Realizing Very High Data Rates Over the Rich-Scattering Wireless Channel", P.W. Wolniansky, et al., IEEE 1998, pp. 295-300.						
"Pilot-aided Adaptive Chip Equalizer Receiver for Interference Suppression in DS-CDMA Forward Link", Frederik Petre et al., IEEE 2000, pp. 303-308							
Examiner's Signature: Date Considered:							
	f reference was considered, whethe				ion if not co	nsidered.	
Include a copy of this citation form with your next correspondence to the Applicant(s).							